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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/812,947 03/27/2001 PAT 6,725,440

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2000-085775 03/27/2000

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged _____ Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 1
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## TITLE

METHOD OF EXPOSING A SEMICONDUCTOR INTEGRATED CIRCUIT INCLUDING DEVICE REGIONS AND  
GLOBAL ROUTING REGION

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